IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Ap	plication)	PATENT APPLICATION
Inventor	(s): Garg, et al.)	
Serial N	lo.: 07/726,773))	
Filed:	July 8, 1991)	
Title:	RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS)	

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name; I believe that I am the original, first and sole inventor (if one name is listed below), first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS

the	specification	of	which	(check	applicable	ones)) :
-----	---------------	----	-------	--------	------------	-------	-----

	is attached hereto;
X	was filed with the above-identified "Filed" date and "Serial No."
-	was amended on (or amended through)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose information which is material to the examination of the application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

(:		me of sole st inventor: _	Sanjiv Garg
(1) Residen —	ce:	46820 Sentinel Drive Fremont, California 94539
(- 1) Post Of -	fice Address:	Same as Residence
•	•	ship:	1
(1) Invento	r's signature	: Sanju Muz
(1) Date: _	9691	
	****	*****	*******
(2) Full na joint	me of second inventor:	Derek J. Lentz
(2) Residen	ce:	17400 Phillips Avenue Los Gatos, California 95032
(2) Post Of	fice Address:	Same as Residence
(2) Invento	or's signature	A. Sulffert
(2) Date: _	7/6/1/	
	****	****	*********
((3) Full na joint	ame of third inventor:	Le Trong Nguyen
. ((3) Resider	nce:	15096 Danielle Place Monte Sereno, California 95030
í	- (3) Post O	ffice Address:	Same as Residence
`			
((3) Citize	nship: <u>Viet</u>	nam /
((3) Invent	or's signature	: 19mga (km - C
((3) Date: .	9/6/91	

(4)	Full name of fourth joint inventor: Sho Long Chen
(4)	Residence: 14411 Ouito Road Saratoga, California 95070
(4)	Post Office Address: Same as Residence
(4)	Citizenship: U.S.A.
(4)	Inventor's signature: Stuffere
(4)	Date: Sept 6, 91

Title 37, Code of Federal Regulations, §1.56(a)

SECTION 1.56. DUTY OF DISCLOSURE; FRAUD; STRIKING OR REJECTION OF APPLICATION.

(a) A duty of candor and good faith toward the Patent and Trademark Office rests on the inventor, on each attorney or agent who prepares or prosecutes the application and on every other individual who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application. All such individuals have a duty to disclose to the Office information they are aware of which is material to the examination of the application. Such information is material when there is a substantial likelihood that a reasonable examiner would consider it important in deciding whether to allow the application to issue as a patent. The duty is commensurate with the degree of involvement in the preparation or prosecution of the application.